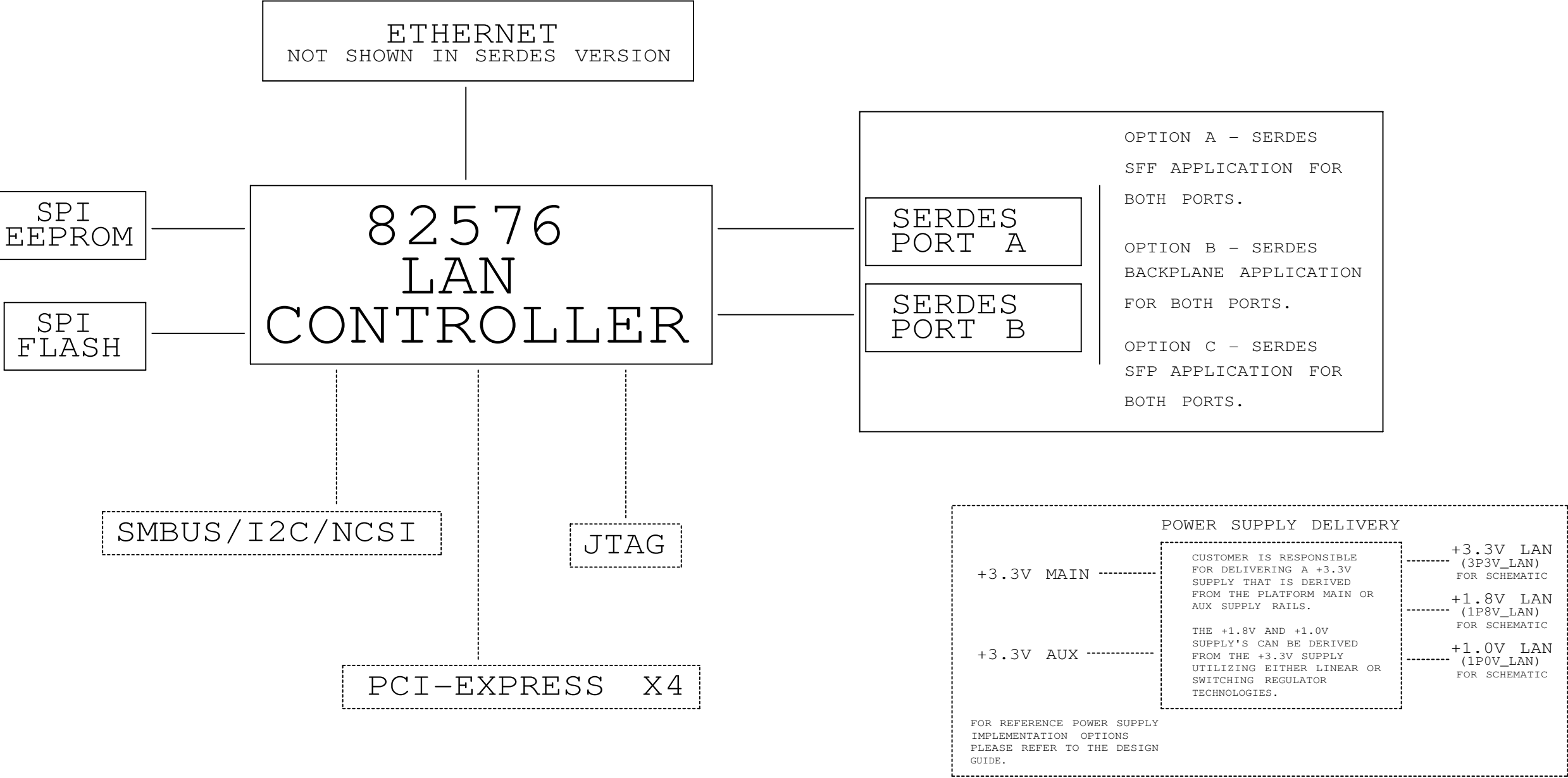


8	7	6	5	4	3	2	1	
D								D
C	82576 REFERENCE DESIGN (SERDES/FIBER/SFP)							C
B								B
A	PAGE INDEX 1 - TITLE PAGE 2 - FUNCTIONAL BLOCK DIAGRAM 3 - 82576 MDI, LEDS, CRYSTAL, SERDES, SFP, NCSI, SMBUS, EEPROM AND FLASH 4 - 82576 VCC3P3, VCC1P8V, VCC1P0V AND VSS 5 - 82576 PCI EXPRESS, JTAG, AUX POWER, LAN DISABLE, MAIN POWER OK AND DEVICE OFF 6 - SERDES FIBER OPTIC OPTION A - SHOWS IMPLEMENTATION FOR BOTH PORT 0 AND 1. 7 - SERDES BACKPLANE OPTION B - SHOWS IMPLEMENTATION FOR BOTH PORT 0 AND 1. 8 - SERDES SFP OPTION C - SHOWS IMPLEMENTATION FOR BOTH PORT 0 AND 1. 1 - TITLE PAGE LAN ACCESS DIVISION 2111 N.E. 25th AVENUE HILLSBORO, OR 97124 TITLE 82576 Reference Deisgn (SERDES/FIBER/SFP) Schematics SIZE B CODE DOCUMENT NUMBER 321785-001 REV 2.1 DATE 02/15/2009 SHEET 1							A
8	7	6	5	4	3	2	1	

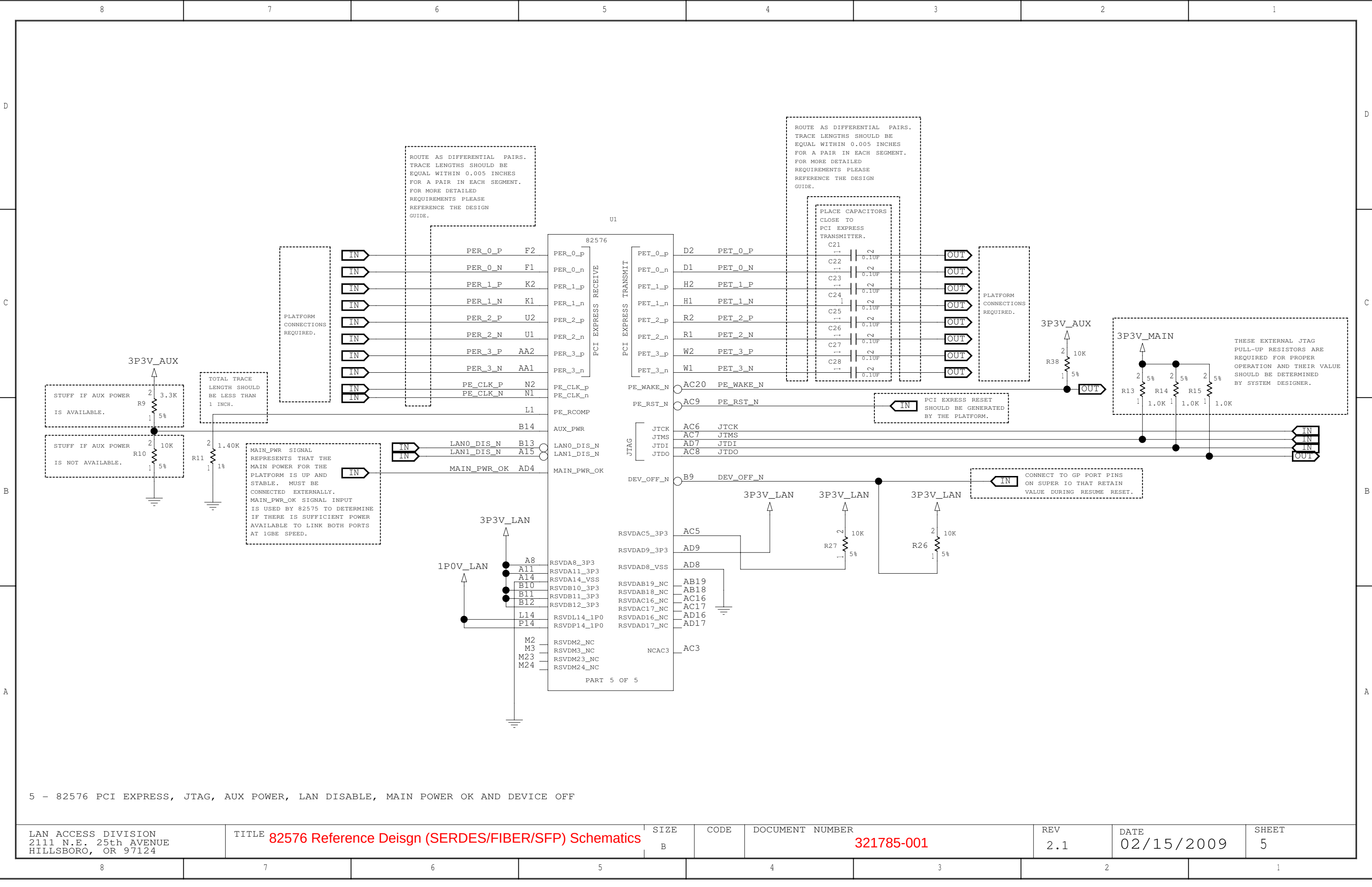
FUNCTIONAL BLOCK DIAGRAM

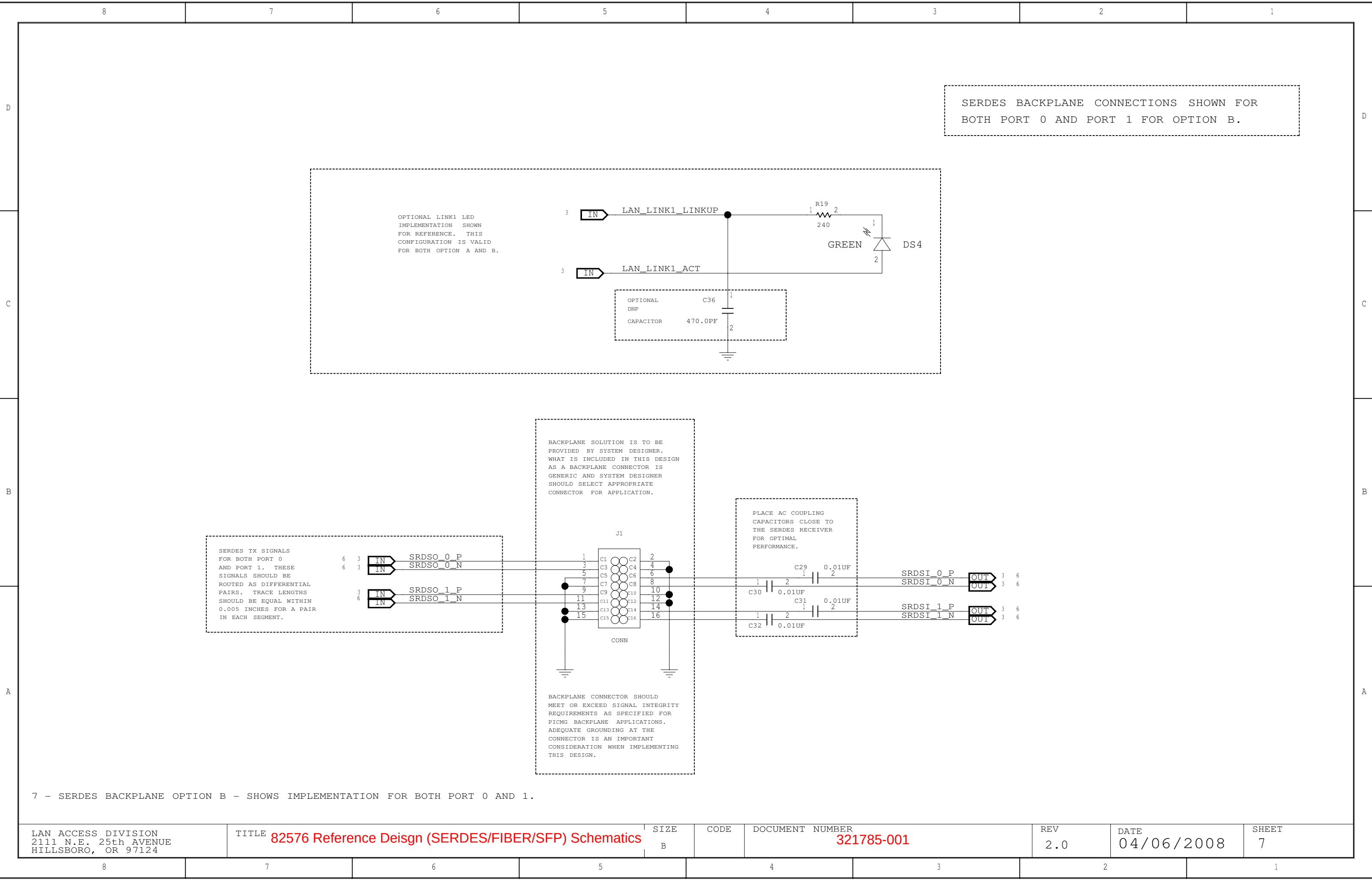


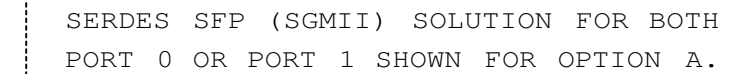
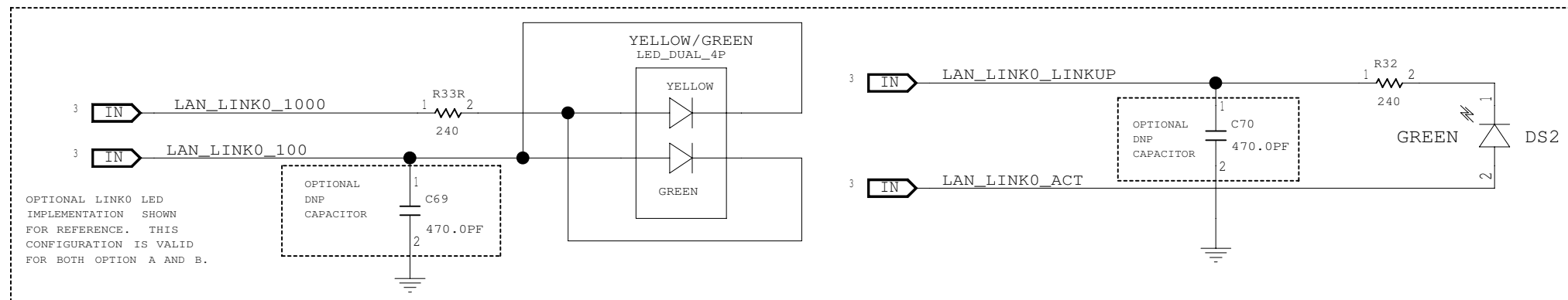
LEGEND

- SOLID LINE INDICATES THAT THE CONNECTIONS ARE SHOWN IN REFERENCE SCHEMATIC.
- DASHED LINE INDICATES THAT THE CONNECTIONS ARE NOT SHOWN IN REFERENCE SCHEMATIC.
- THESE CONNECTIONS ARE OWNED BY THE SYSTEM DESIGNER.

2 - FUNCTIONAL BLOCK DIAGRAM

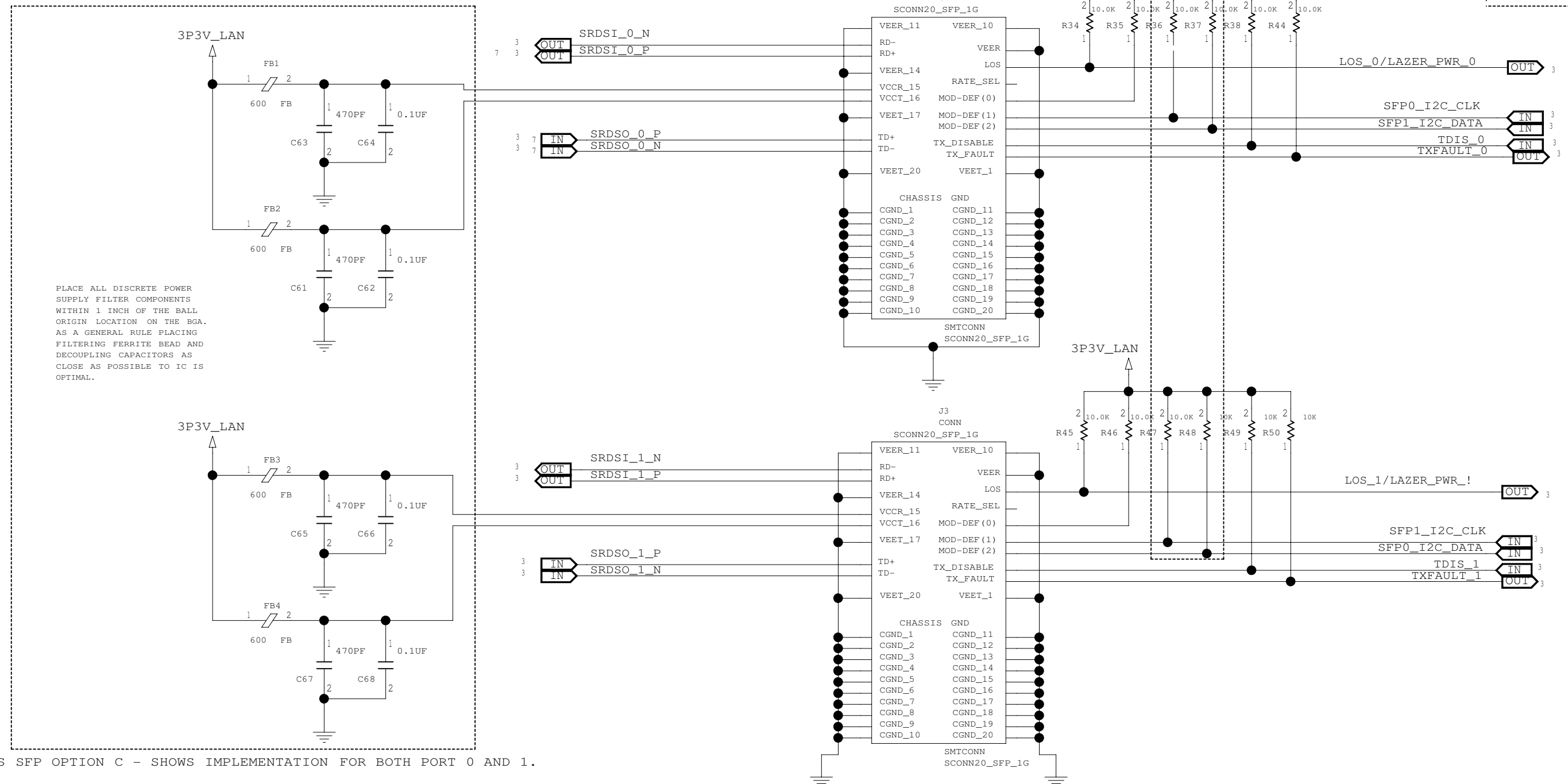






THESE EXTERNAL I2C
PULL-UP RESISTORS ARE
REQUIRED FOR PROPER
OPERATION AND THEIR VALUE
SHOULD BE DETERMINED
BY SYSTEM DESIGNER.

WHEN THESE PINS ARE
PROGRAMMED AS MDIO THESE
RESISTORS SHOULD NOT BE
POPULATED.



8 - SERDES SFP OPTION C - SHOWS IMPLEMENTATION FOR BOTH PORT 0 AND 1.